

RECEIVED  
CENTRAL FAX CENTER

MAY 09 2006

**Fast-Disabled Voltage Regulator Circuit with Low-Noise Feedback Loop and  
Operating Method Thereof**

Appl. No.	:	10/711,618	Confirmation No. 5617
Applicant	:	Ling-Wei Ke, Chi-Kun Chiu	
Filed	:	September 29, 2004	
TC/A.U.	:	2838	
Examiner	:	Sterrett, Jeffrey L	
Docket No.	:	MTKP0080USA0	
Customer No.	:	27765	

Commissioner for Patents  
P.O. Box 1450  
Alexandria VA 22313-1450

**INTRODUCTORY COMMENTS**

5 In response to the Office action of January 25, 2006, please amend the above-identified application as follows:

**Amendments to the Specification begin on page 2 of this paper.  
Remarks begin on page 3 of this paper.**